

U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/802,551

Filing date: March 9, 2001

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Inventors: Clive M. Philbrick et al.

Group Art Unit: 2182

Examiner name: unknown

Attorney Docket No. ALA-012

INTELLIGENT NETWORK STORAGE INTERFACE

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## U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	4,366,538	12/28/82	Johnson et al.	364	200	
	B	4,991,133	2/5/91	Davis et al.	364	900	
	C	5,056,058	10/08/91	Hirata et al.	709	230	
	D	5,097,442	03/17/92	Ward et al.	365	78	
	E	5,163,131	11/10/92	Row et al.	395	200	
	F	5,212,778	05/18/93	Dally et al.	395	400	
	G	5,280,477	01/18/94	Trapp	370	85.1	
	H	5,289,580	02/22/94	Latif et al.	395	275	
	I	5,303,344	04/12/94	Yokoyama et al.	395	275	
	J	5,412,782	05/02/95	Hausman et al.	395	250	
	K	5,448,566	09/05/95	Richter et al.	370	94.1	

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## Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	L	WO 00/13091	03/09/00	PCT/US98/24943				
	M	WO 99/65219	12/16/99	PCT/US99/13184				

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

N	Internet pages entitled "Hardware Assisted Protocol Processing", (which Eugene Feinber is working on), 1 page, printed 11/25/98.
O	Zilog product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages, 1997.
P	Internet pages of Xpoint Technologies www.xpoint.com website, 5 pages, printed 12/19/97.
Q	Internet pages entitled: Asante and 100BASE-T Fast Ethernet. 7 pages, printed 5/27/97.

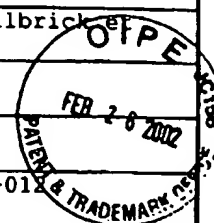
Examiner

Date Considered

8/19/04

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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INTELLIGENT NETWORK STORAGE INTERFACE SYSTEM	Examiner name: unknown
	Attorney Docket No. ALA-012



### U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
<i>[Signature]</i>	A	5,485,579	01/16/96	Hitz et al.	395	200.12	
<i>[Signature]</i>	B	5,506,966	04/09/96	Ban	395	250	
<i>[Signature]</i>	C	5,511,169	04/23/96	Suda	395	280	
<i>[Signature]</i>	D	5,548,730	08/20/96	Young et al.	395	280	
<i>[Signature]</i>	E	5,588,121	12/24/96	Reddin et al.	395	200.15	
<i>[Signature]</i>	F	5,566,170	10/15/96	Bakke et al.	370	60	
<i>[Signature]</i>	G	5,590,328	12/31/96	Seno et al.	395	675	
<i>[Signature]</i>	H	5,592,622	01/07/97	Isfeld et al.	395	200.02	
<i>[Signature]</i>	I	5,629,933	05/13/97	Delp et al.	370	41	
<i>[Signature]</i>	J	5,634,099	05/27/97	Andrews et al.	395	200.07	
<i>[Signature]</i>	K	5,634,127	05/27/97	Cloud et al.	395	680	

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### Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
<i>[Signature]</i>	L	WO 01/05107 A1	01/18/01	PCT/US00/19006				
<i>[Signature]</i>	M	WO 01/05116 A2	01/18/01	PCT/US00/19243				

### OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>[Signature]</i>	N	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University. 13 pages, printed 12/21/98.
<i>[Signature]</i>	O	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
<i>[Signature]</i>	P	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
<i>[Signature]</i>	Q	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996.

Examiner *[Signature]* Date Considered 8/19/04

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